

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	42.0" WUXGA TFT LCD
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BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC420WU6
SUFFIX	SLA1(RoHS Verified)

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE	DATE
/		
/		
/		

Please return 1 copy for your confirmation with
your signature and comments.

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TV Product Development Dept.
LG. Philips LCD Co., Ltd

Product Specification

CONTENTS

Number	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTERISTICS	6
3-2	INTERFACE CONNECTIONS	10
3-3	SIGNAL TIMING SPECIFICATIONS	11
3-4	SIGNAL TIMING WAVEFORMS	13
3-5	COLOR DATA REFERENCE	14
3-6	POWER SEQUENCE	15
4	OPTICAL SPECIFICATIONS	17
5	MECHANICAL CHARACTERISTICS	21
6	RELIABILITY	24
7	INTERNATIONAL STANDARDS	25
7-1	SAFETY	25
7-2	EMC	25
8	PACKING	26
8-1	DESIGNATION OF LOT MARK	26
8-2	PACKING FORM	26
9	PRECAUTIONS	27
9-1	MOUNTING PRECAUTIONS	27
9-2	OPERATING PRECAUTIONS	27
9-3	ELECTROSTATIC DISCHARGE CONTROL	28
9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	28
9-5	STORAGE	28
9-6	HANDLING PRECAUTIONS FOR PROTECTION FILM	28
Appendix		29~33

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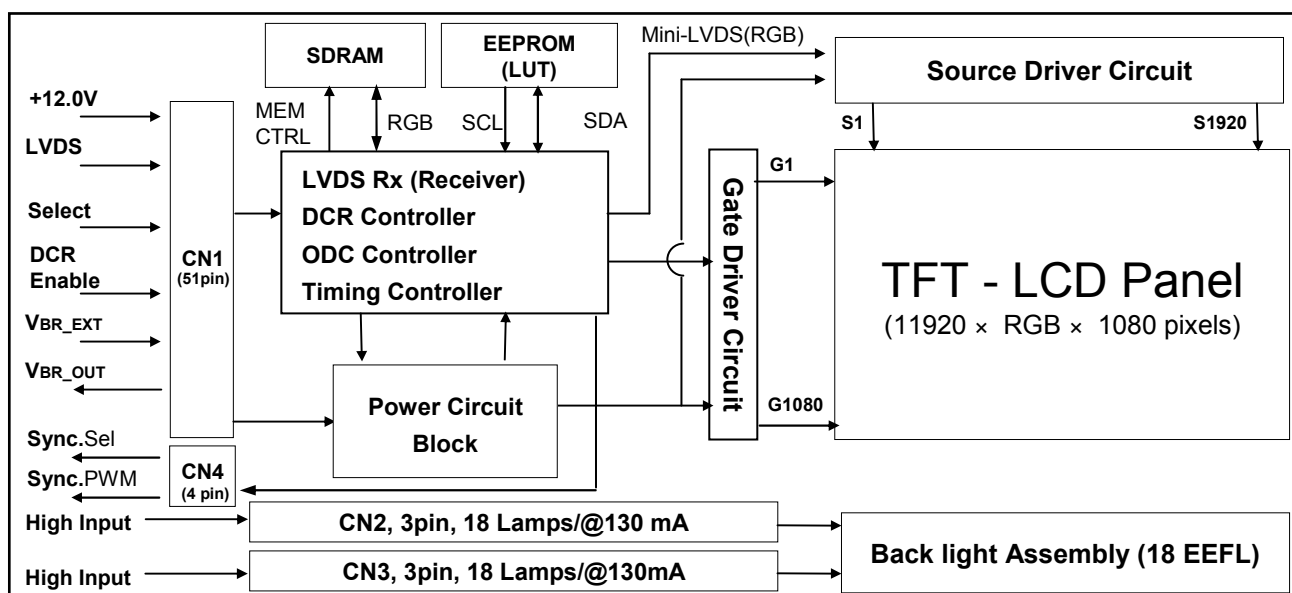
Product Specification

1. General Description

The LC420WU6 is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 42.02 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.07 Billion(FRC) colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.02 inches(1067.31mm) diagonal
Outline Dimension	983.0(H) x 576.0 (V) x 47.3 mm(D) (Typ. Without inverter)
Pixel Pitch	0.4845 mm x 0.4845 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit FRC, 1.07Billion colors
Luminance, White	500 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total 172 W (Typ.) (Logic = 8.8 W, Inverter =163.2 W [IRMS=])
Weight	(11.0Kg) (Typ.) (12.0Kg) (Max.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 13%)

Product Specification

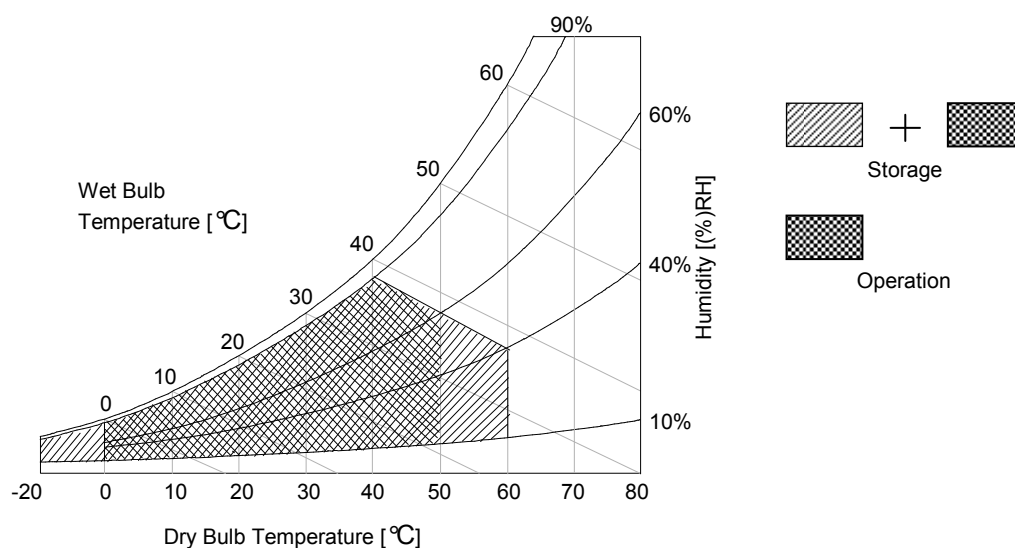
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Remark
			Min	Max		
Power Input Voltage	LCD circuit	V _{LCD}	-0.3	+14.0	V [DC]	at 25 ± 2 °C
Operating Temperature		T _{OP}	0	+50	°C	Note 1
Storage Temperature		T _{ST}	-20	+60	°C	
Operating Ambient Humidity		H _{OP}	10	90	%RH	
Storage Humidity		H _{ST}	10	90	%RH	

- Notes : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max, and no condensation.
2. Gravity mura can be guaranteed under 40 °C condition.



Product Specification

3. Electrical Specifications

3-1. Electrical Characteristics

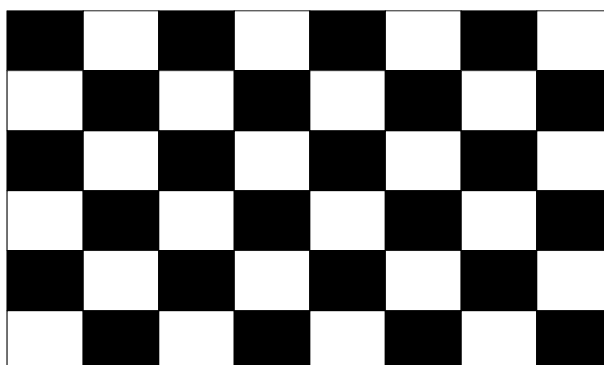
It requires two power inputs. One is employed to power for the LCD circuit. The other is used for the EEFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min	Typ	Max		
Circuit :						
Power Input Voltage	VLCD	11.4	12.0	12.6	VDC	
Power Input Current	ILCD	-	734	954	mA	1
		-	866	1125	mA	2
Power Consumption	PLCD	-	8.8	11.48	Watt	1
Rush current	IRUSH	-	-	3	A	3

- Notes : 1. The specified current and power consumption are under the V_{LCD}=12.0V, 25 ± 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 2. The current is specified at the maximum current pattern.
 3. The duration of rush current is about 2ms and rising time of power input is 1ms (min)

White : 1023Gray
 Black : 0Gray



Mosaic Pattern(8 x 6)

Product Specification

Table 3. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Backlight Assembly :							
Operating Voltage (one side,fBL=46KHZ, IBL= 129.6mArms))		VBL	-	950	-	V _{RMS}	1, 2
Operating Current (one side)		IBL	-	130	-	mA _{RMS}	1
Established Starting Voltage (one side)	0℃	VS	-	-	1350	V _{RMS}	1, 3
	25℃		-	-	1100		
Operating Frequency		fBL	44	46	48	KHz	4
Striking Time		S TIME	-	-	1.5	sec	3
Power Consumption		PBL		163.2		Watt	6
Burst Dimming Duty		PWM duty	20		100	%	9
Burst Dimming Frequency		1/T	150	-	180	Hz	9

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Lamp : (APPENDIX-V)							
Lamp Voltage (one side)		VLAMP	850	1000	1050	V _{RMS}	1, 2
Lamp Current (one side)		ILAMP	-	7.2	-	mA _{RMS}	1
Discharge Stabilization Time		TS	-	-	3	Min	1, 5
Lamp Frequency		f LAMP	41	46	51	KHz	
Lamp Temperature		TLAMP			130	°C	
Established Starting Voltage(one side)	0 °C	Vs			1350	V _{RMS}	1, 3
	25 °C	Vs			1100		
Life Time			50,000			Hrs	7

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

The electrical characteristics of inverter are based on High-High Driving type.

The performance of the lamps in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So, all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

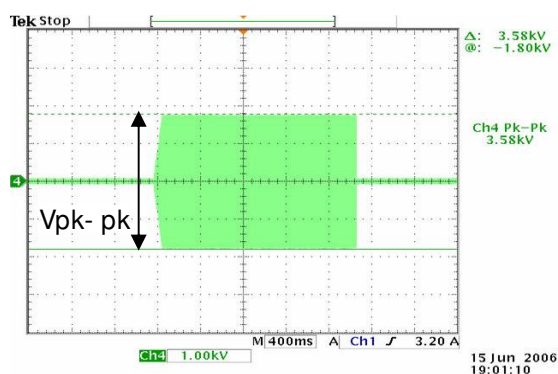
When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD- Assembly should be operated in the same condition as installed in your instrument.

※ Do not attach a conductive tape to lamp connecting wire.

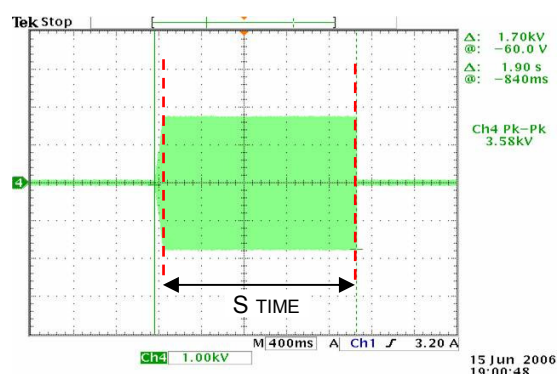
If you attach conductive tape to the lamp wire, not only luminance level can be lower than typical one but also inverter operate abnormally on account of leakage current which is generated between lamp wire and conductive tape.

- Specified values are defined for a Backlight Assembly. (IBL : 18 lamp, 7.2mA/Lamp)
- Operating voltage is measured at 25 ± 2 °C(after 2hr.aging). The variance range for operating voltage is $\pm 10\%$.
- The established starting voltage [Vs] should be applied to the lamps for more than Striking time (S TIME) for start-up. Inverter open voltage must be more than established starting voltage. Otherwise, the lamps may not be turned on. The used lamp current is typical value.

Product Specification



$$V_s = (V_{pk-pk}) / [2 \cdot \sqrt{2}]$$



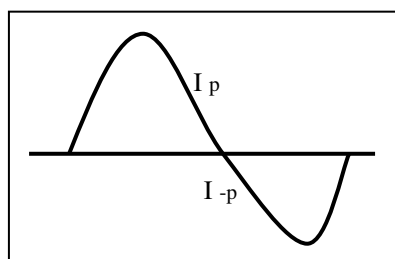
4. Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore, lamp frequency shall be as away possible from the horizontal synchronous frequency and its harmonics range in order to prevent interference.
5. The brightness of the lamp after lighted for 5minutes is defined as 100%.
 T_s is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
 The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
6. Maximum level of power consumption is measured at initial turn on.
 Typical level of power consumption is measured after 2hrs aging at $25 \pm 2^\circ\text{C}$.
7. The life time is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^\circ\text{C}$, based on duty 100%.
8. The output of the inverter must have symmetrical(negative and positive) voltage and current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has not only unsymmetrical voltage and current but also spike wave.

Requirements for a system inverter design, which is intended to achieve better display performance, power efficiency and more reliable lamp characteristics.

It can help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$|I_p - I_{-p}| / I_{op} \times 100\%$$

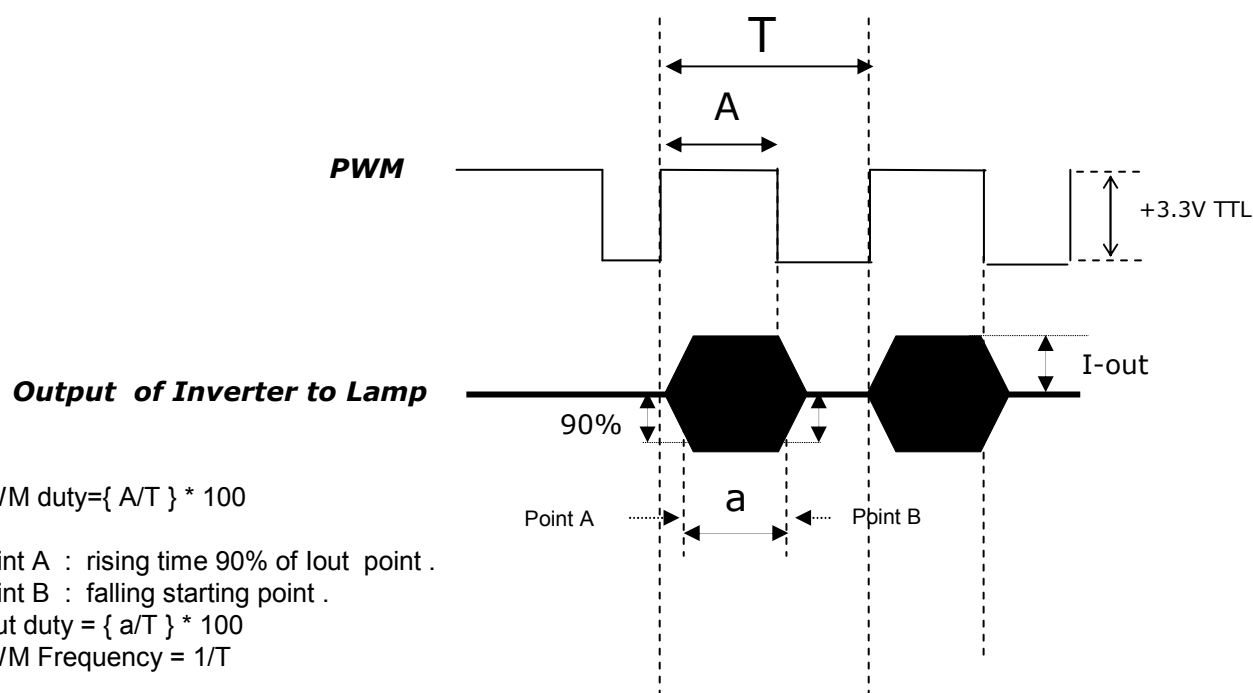
* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{op}$$

Product Specification

9. The reference method of burst dimming duty ratio.

It is recommended to use synchronous V-sync frequency for prevent waterfall(Vsync x 3 =Burst Frequency)



$$PWM \text{ duty} = \{ A/T \} * 100$$

Point A : rising time 90% of Iout point .

Point B : falling starting point .

$$I \text{ out duty} = \{ a/T \} * 100$$

$$PWM \text{ Frequency} = 1/T$$

- ※ We recommend not to be much different between PWM duty and Iout duty .
- ※ Dimming current output rising and falling time may produce humming and inverter trans' sound noise.
- ※ Burst dimming duty should be 100% for 1second after turn on
- ※ Equipment
 - Oscilloscope : TDS3054B(Tektronix)
 - Current Probe : P6022 AC (Tektronix)
 - High Voltage Probe: P5100(Tektronix)

10. The Cable between the backlight connector and its inverter power supply should be connected directly with a minimized length. The longer cable between the backlight and the inverter may cause the lower luminance of lamp and may require more higher starting voltage (Vs).

11. The operating current must be measured as near as backlight assembly input.

12. The operating current unbalance between left and right must be under typ $\pm 10\%$.

Product Specification

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and Master 3-pin and Slave 3-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose)
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	Reserved	Ground
2	NC	No Connection	28	RE0N	SECOND CHANNEL 0-
3	NC	No Connection	29	RE0P	SECOND CHANNEL 0+
4	NC	No Connection	30	RE1N	SECOND CHANNEL 1-
5	NC	No Connection	31	RE1P	SECOND CHANNEL 1+
6	NC	No Connection	32	RE2N	SECOND CHANNEL 2-
7	LVDS Select	'H' = JEIDA , 'L' = VESA	33	RE2P	SECOND CHANNEL 2+
8	VBR_EXT	External VBR	34	GND	Ground
9	VBR_OUT	VBR output	35	RECLKN	SECOND CLOCK CHANNEL C-
10	DCR_Enable	'H' = Enable , 'L' = Disable	36	RECLKP	SECOND CLOCK CHANNEL C+
11	GND	Ground	37	GND	Ground
12	RO0N	FIRST CHANNEL 0-	38	RE3N	SECOND CHANNEL 3-
13	RO0P	FIRST CHANNEL 0+	39	RE3P	SECOND CHANNEL 3+
14	RO1N	FIRST CHANNEL 1-	40	RE4N	SECOND CHANNEL 4-
15	RO1P	FIRST CHANNEL 1+	41	RE4P	SECOND CHANNEL 4+
16	RO2N	FIRST CHANNEL 2-	42	Reserved	No connection or GND
17	RO2P	FIRST CHANNEL 2+	43	Reserved	No connection or GND
18	GND	Ground	44	GND	Ground
19	ROCLKN	FIRST CLOCK CHANNEL C-	45	GND	Ground
20	ROCLKP	FIRST CLOCK CHANNEL C+	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	RO3N	FIRST CHANNEL 3-	48	VLCD	Power Supply +12.0V
23	RO3P	FIRST CHANNEL 3+	49	VLCD	Power Supply +12.0V
24	RO4N	FIRST CHANNEL 4-	50	VLCD	Power Supply +12.0V
25	RO4P	FIRST CHANNEL 4+	51	VLCD	Power Supply +12.0V
26	Reserved	No connection or GND	-	-	-

Notes :

1. The pin no 44 is LCD Test option. "AGP" (Auto Generation LCM operates Pattern) or "NSB" (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 volt power supply. LPL recommends "NSB". (AGP : "VCC" or "OPEN" / NSB : "GND")
2. All GND(ground) pins should be connected together to the LCD module's metal frame.
3. All VLCD (power input) pins should be connected together.
4. All Input levels of LVDS signals are based on the IEA 664 Standard.
5. Specific pins(pin No. #1~#10) are used for internal data process of the LCD module.
If not used, these pins are no connection.
6. If DCR function should be enable('H'), 10th pin must be connected to serial resistor which value is under 1k ohm.

Product Specification

3-2-2. Backlight Module

[Master]

1) Balance Connector

: 65002WS-03 (manufactured by YEONHO) or equivalent

2) Mating Connector

: 65002HS-03 (manufactured by YEONHO) or equivalent.

[Slave]

1) Balance Connector

: 65002WS-03 (manufactured by YEONHO) or equivalent

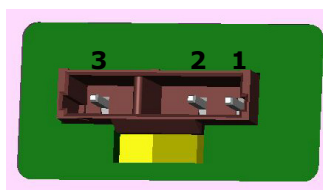
2) Mating Connector

: 65002HS-03 (manufactured by YEONHO) or equivalent.

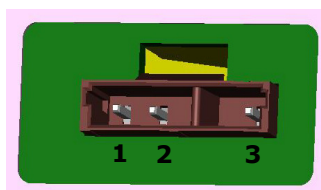
Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3)

No	Symbol	Master	Slave	Note
1	H_Input	High_Input	High_Input	
2	H_Input	High_Input	High_Input	
3	FB	NC	NC	

◆ Rear view of LCM



Master



Slave

3-2-3. Sync connection between LCD Module and System

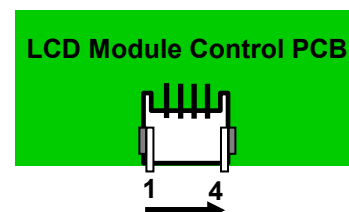
There are 4 pins sync connector in the LCD module control PCB. The No.1 pin is for REFMODE which is selected of Vsync frequency signal. The No.2 pin is for WPWM which is generated for PWM frequency synchronically between control & inverter. The rest pins are for Ground and NC.

1) Inverter Sync Connector

: DF14-4S-1.25C (manufactured by Hirose) or equivalent

Table 6. INVERTER SYNC. CONNECTOR PIN CONFIGURATION(CN4)

Pin No.	Symbol	Description	Note
1	REFMODE	Vsync. Frequency select signal	
2	WPWM	PWM Frequency output	
3	GND	Ground	
4	NC	No connection	



Notes:

1. LPL recommends that Inverter Burst dimming frequency (PWM Frequency) should be synchronized with Sync signal.
2. LPL recommends that Inverter Burst dimming frequency (PWM Frequency) should be 150Hz or 180Hz.

Product Specification

3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE for NTSC (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	t _{HV}	-	960	-	tclk	
	Blank	t _{HB}	100	140	320	tclk	
	Total	t _{HP}	1060	1100	1280	tclk	2200/2
Vertical	Display Period	t _{VV}	-	1080	-	Lines	
	Blank	t _{VB}	12	45	68	Lines	
	Total	t _{VP}	1092	1125	1148	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	f _{CLK}	71.55	74.25	77	MHz	148.5/2
	Horizontal	f _H	65.5	67.5	68.9	KHz	
	Vertical	f _V	57	60	63	Hz	

Table 7 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

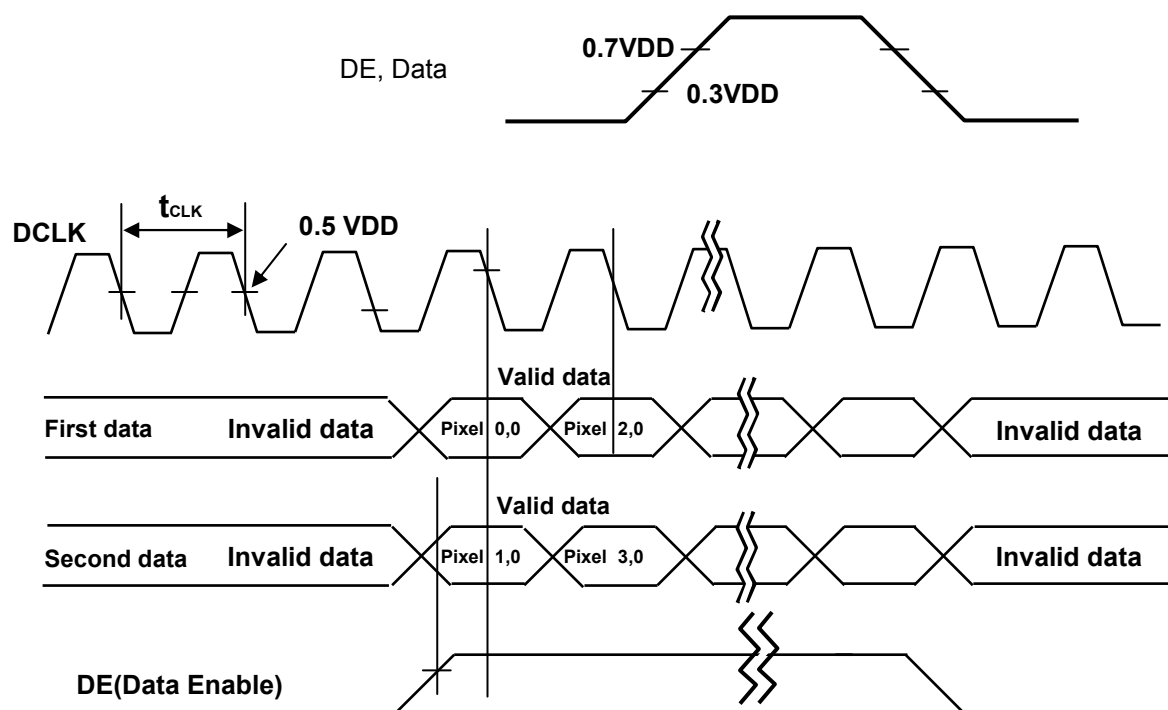
Table 7. TIMING TABLE for PAL (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	t _{HV}	-	960	-	tclk	
	Blank	t _{HB}	100	140	240	tclk	
	Total	t _{HP}	1060	1100	1200	tclk	2200/2
Vertical	Display Period	t _{VV}	-	1080	-	Lines	
	Blank	t _{VB}	25	45	65	Lines	
	Total	t _{VP}	1105	1125	1145	Lines	

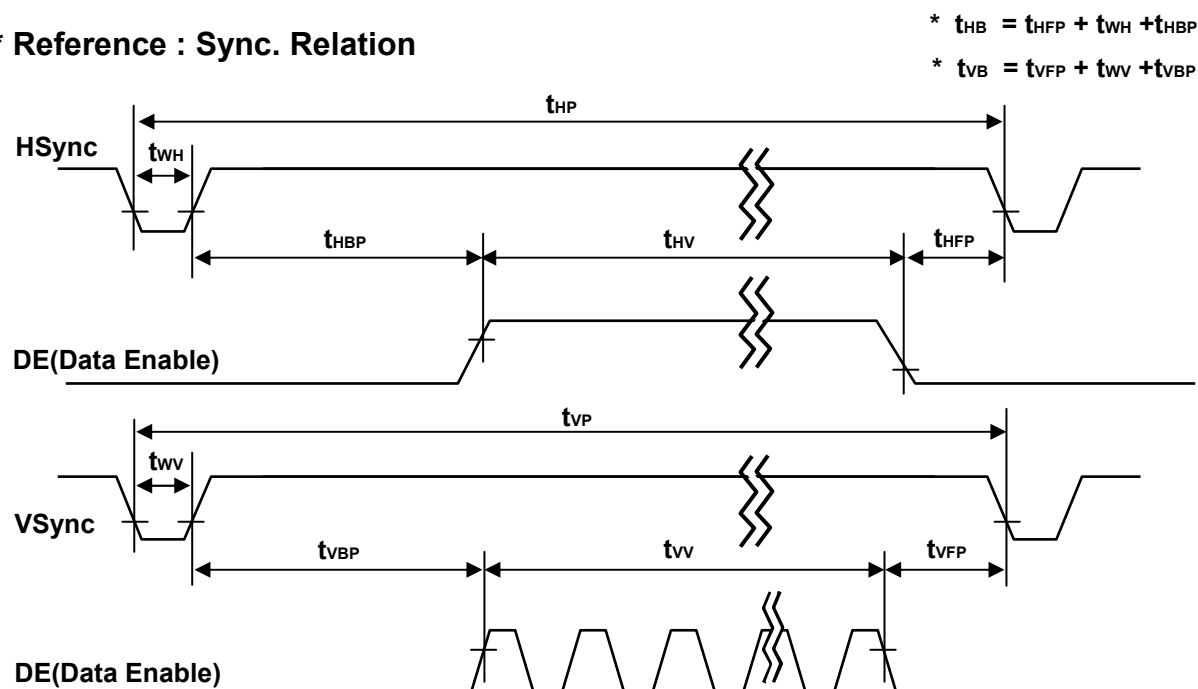
ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	f _{CLK}	58.3	61.88	65.5	MHz	123.75/2
	Horizontal	f _H	55.25	56.25	57.25	KHz	
	Vertical	f _V	47	50	53	Hz	

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).
The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

3-4. Signal Timing Waveforms



* Reference : Sync. Relation



Product Specification

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

Color		Input Color Data																															
		RED										GREEN										BLUE											
		MSBLSB										MSBLSB										MSBLSB											
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0		
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
RED	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
											
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
GREEN	GREEN (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
											
	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	
BLUE	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
											
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	

Product Specification

3-6. Power Sequence

3-6-1. LCD Driving circuit

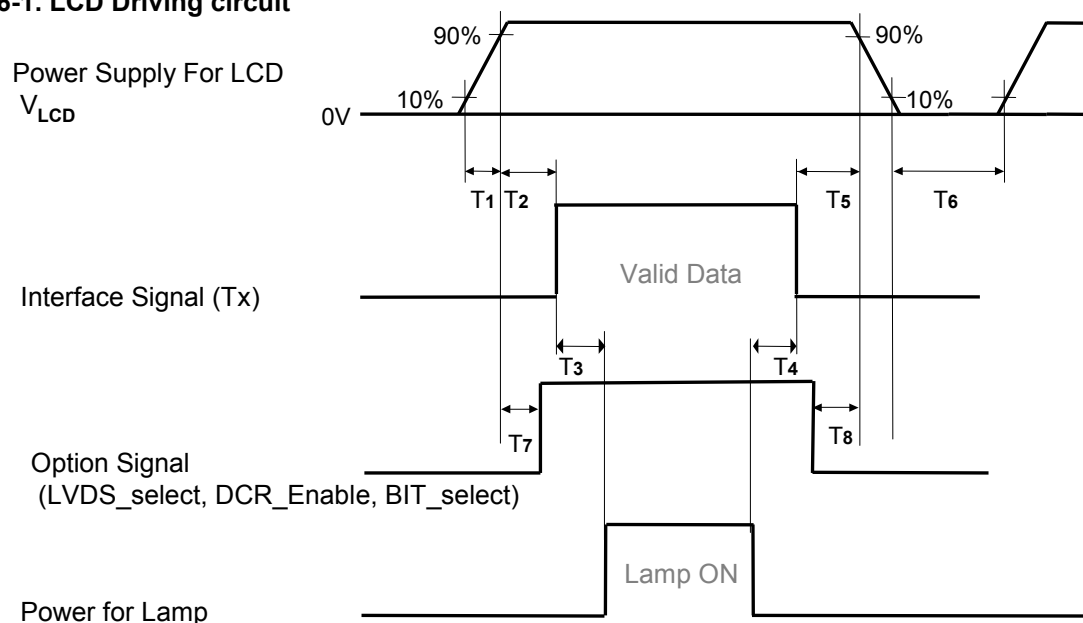


Table 9. POWER SEQUENCE

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5	-	20	ms	
T2	0.5	-	$3 \times (1/f_V)$	ms	3,5
T3	200	-	-	ms	4
T4	200	-	-	ms	4
T5	0	-	-	ms	3,5
T6	2.0	-	-	s	2,6
T7	0	-	T2	ms	5
T8	0	-	T5	ms	5

- Note :
1. Please avoid floating state of interface signal at invalid period.
 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
 3. The case when the T2/T5 exceed $3 \times (1/f_V)$, it operates protection pattern (Black pattern) till valid signal inputted. There is no reliability problem. (ex. 60Hz : $3 \times (1/60\text{Hz}) = 50\text{ms}$)
 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 5. If the on time of signals (Interface signal and Option signals) precedes the on time of Power (V_{LCD}), check the LCD logic Power (V_{CC}) is under 0.8V, otherwise it will be happened abnormal display.
 6. T6 should be measured after the Module has been fully discharged between power off and on period.

Product Specification

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25 \pm 2^\circ\text{C}$. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

FIG. 1 shows additional information concerning the measurement equipment and method.

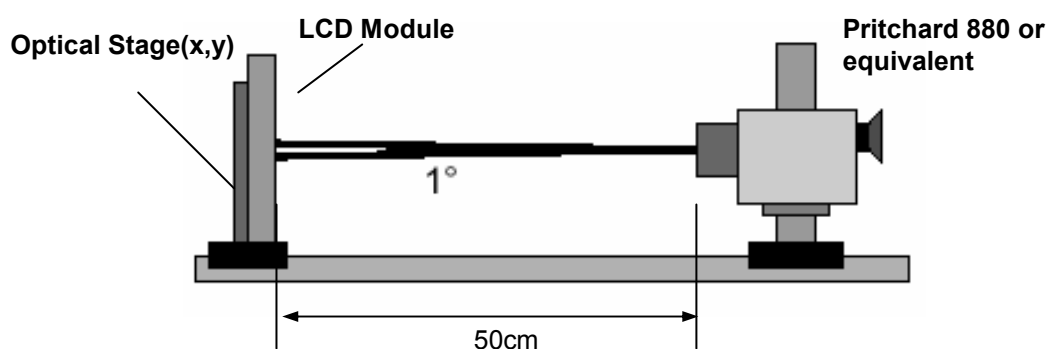


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 11. OPTICAL CHARACTERISTICS

$T_a = 25 \pm 2^\circ\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 148.5\text{MHz}$, $V_{\text{BR_A}} = 1.65\text{V}$, $V_{\text{BR_B}} = 3.3\text{V}$

Parameter		Symbol		Value			Unit	Note
				Min	Typ	Max		
Contrast Ratio		CR		700	1000	-		1
Surface Luminance, white		L _{WH}		400	500	-	cd/m ²	2
Luminance Variation		δ _{WHITE}	5P	-	-	1.3		3
Response Time	Gray-to-Gray	G to G		-	5	8	ms	4,5
Color Coordinates [CIE1931]	RED	Rx		Typ -0.03	0.640	Typ +0.03		
		Ry			0.343			
	GREEN	Gx			0.287			
		Gy			0.613			
	BLUE	Bx			0.144			
		By			0.065			
	WHITE	Wx			0.279			
		Wy			0.292			
Viewing Angle (CR>10)								
	x axis, right(φ=0 °)	θr		89	-	-	degree	6
	x axis, left (φ=180 °)	θl		89	-	-		
	y axis, up (φ=90 °)	θu		89	-	-		
	y axis, down (φ=270 °)	θd		89	-	-		
Gray Scale				-	-	-		7

Product Specification

Notes :1. Contrast Ratio(CR) is defined mathematically as :

CR(Contrast Ratio) = Maximum CR_n (n=1, 2, 3, 4, 5)

$$CR_n = \frac{\text{Surface Luminance at position n with all white pixels}}{\text{Surface Luminance at position n with all black pixels}}$$

n = the Position number(1, 2, 3, 4, 5). For more information, see FIG. 2.

2. Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25± 2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.

3. The variation in surface luminance, δ WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5})$$

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .

For more information, see the FIG. 2.

4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.

6. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 12.

Table 12. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
L0	0.10
L63	0.26
L127	1.16
L191	2.61
L255	4.80
L319	7.77
L383	11.6
L447	16.2
L511	21.7
L575	28.2
L639	35.5
L703	43.8
L767	53.0
L831	63.3
L895	74.5
L959	86.7
L1023	100

Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

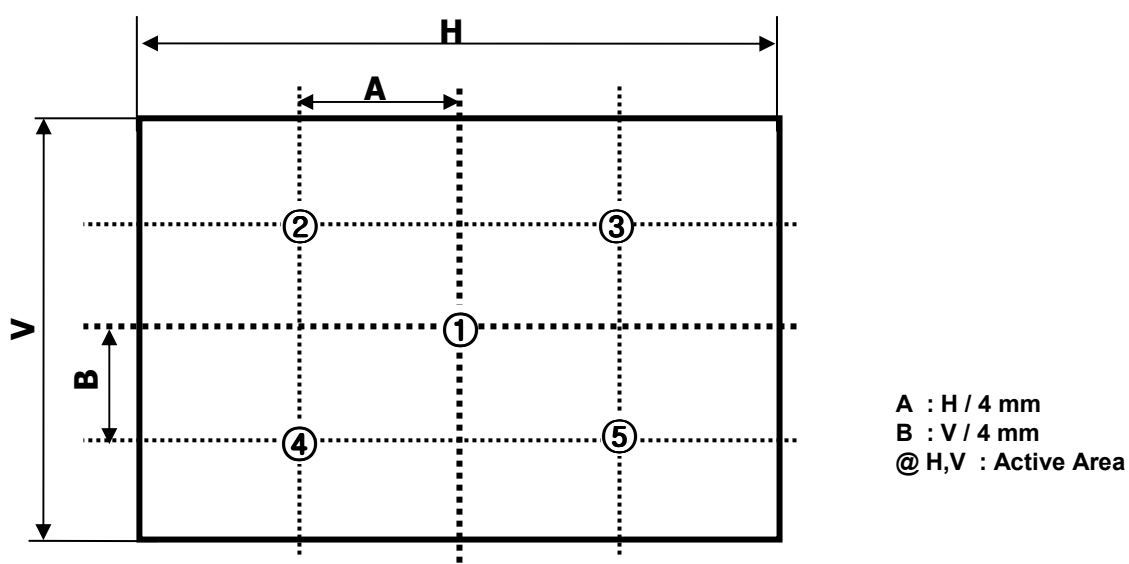


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

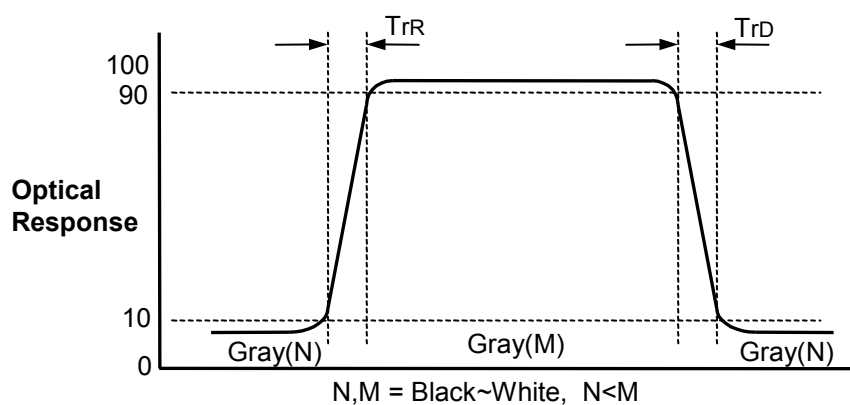


FIG. 3 Response Time

Product Specification

Dimension of viewing angle range

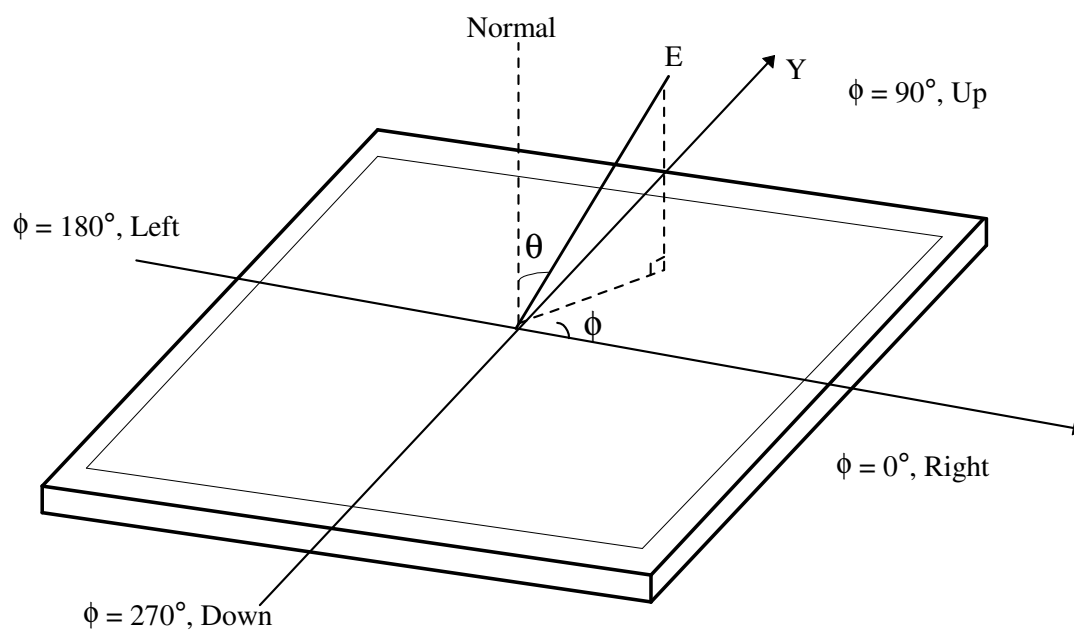


FIG. 4 Viewing Angle

Product Specification

5. Mechanical Characteristics

Table 13 provides general mechanical characteristics.

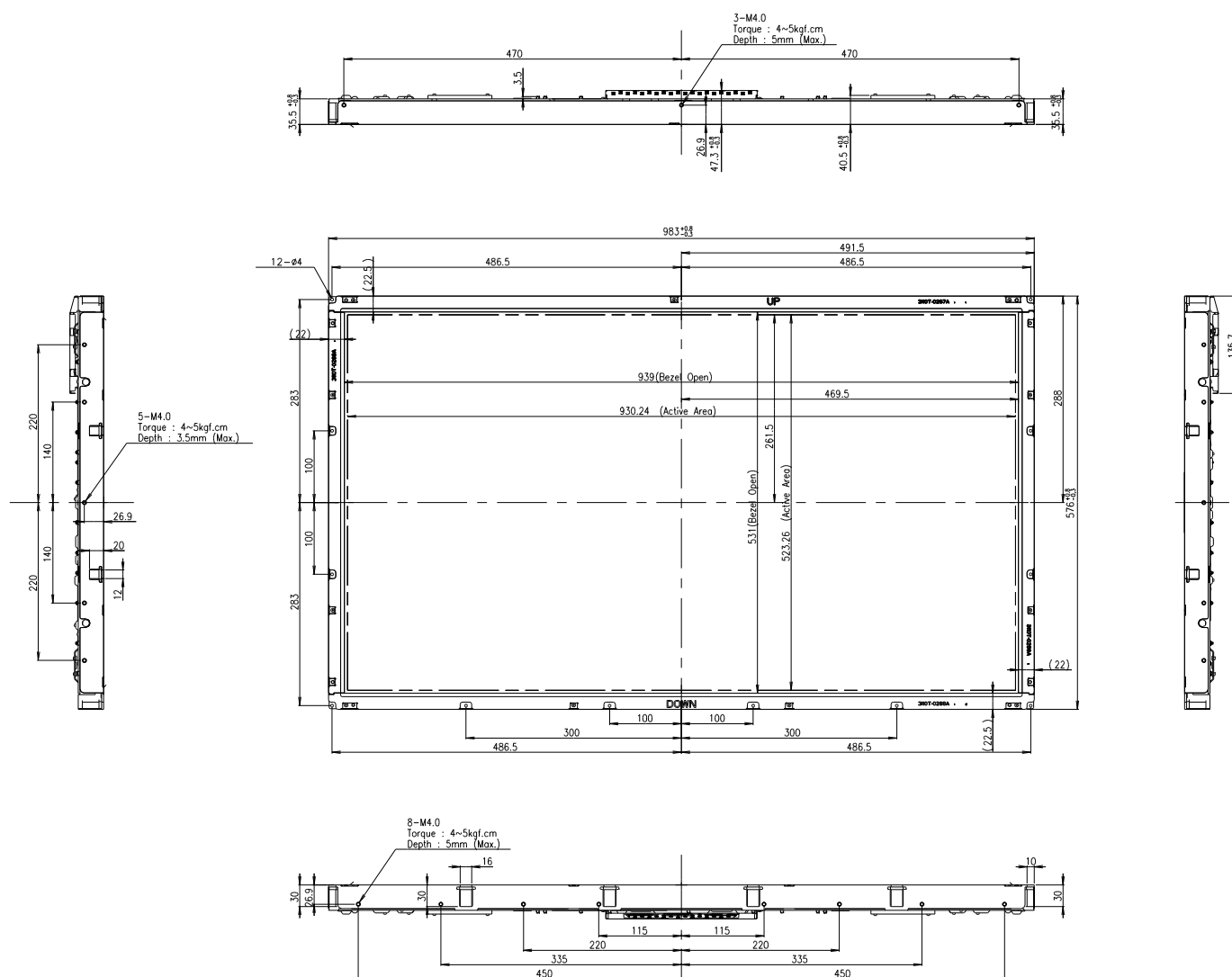
Table 13. MECHANICAL CHARACTERISTICS

Item	Value	
Outline Dimension	Horizontal	983.0 mm
	Vertical	576.0 mm
	Depth	47.3 mm
Bezel Area	Horizontal	939.0 mm
	Vertical	531.0 mm
Active Display Area	Horizontal	930.25 mm
	Vertical	523.26 mm
Weight	11.0 Kg (Typ.) , 12.0 Kg (Max.)	

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

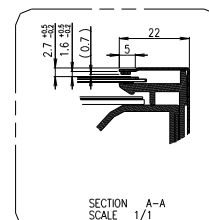
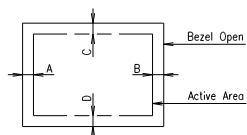
Product Specification

<FRONT VIEW>



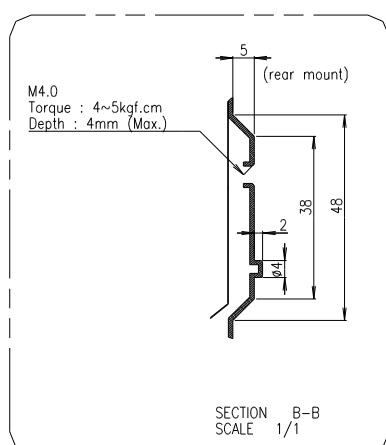
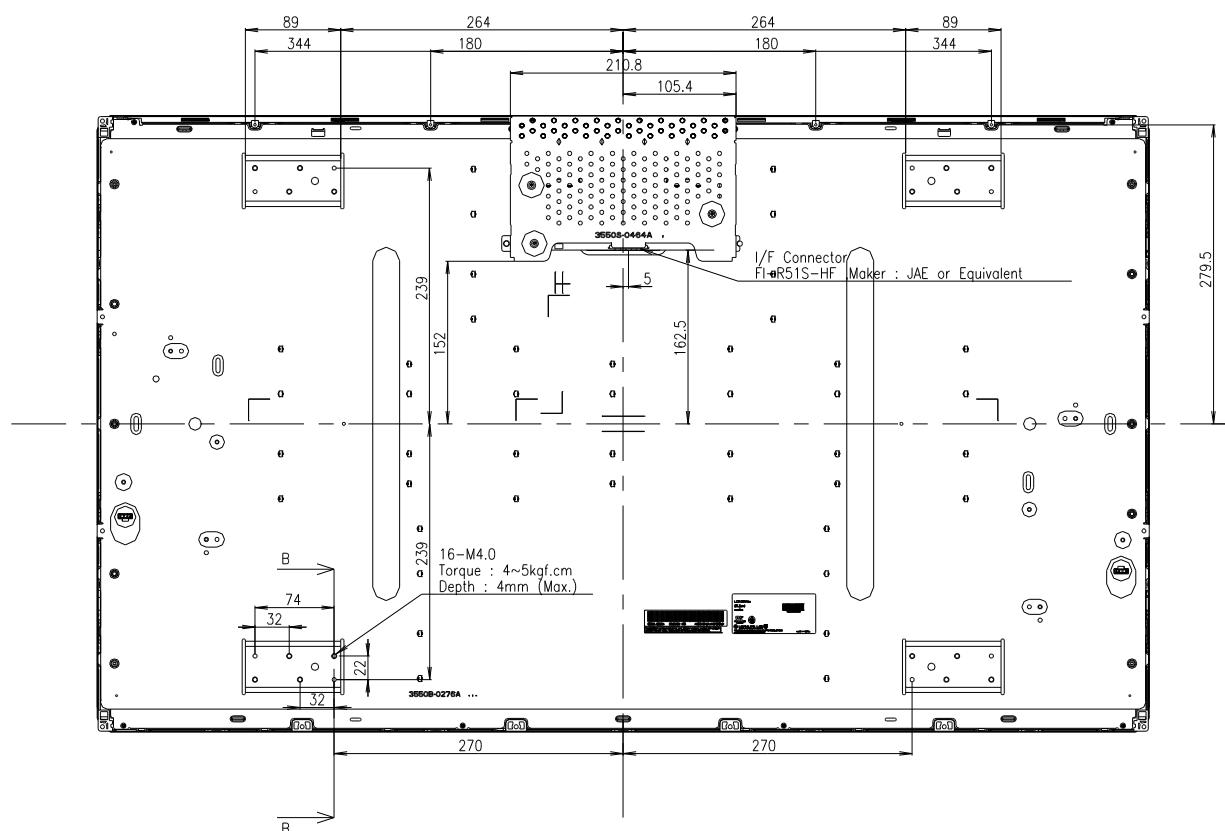
NOTES

1. UNSPECIFIED DIMENSIONAL TOLERANCES TO BE $\pm 0.5\text{mm}$.
2. TILT AND A PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA ARE AS FOLLOW.
 - 1) X-DIRECTION : $|A-B| < 1.5\text{mm}$
 - 2) Y-DIRECTION : $|C-D| < 1.5\text{mm}$



Product Specification

<REAR VIEW>



Product Specification

6. Reliability

Table 14. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60℃ 240h
2	Low temperature storage test	Ta= -20℃ 240h
3	High temperature operation test	Ta= 50℃ 50%RH 240h
4	Low temperature operation test	Ta= 0℃ 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : ±X, ±Y, ±Z One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
- b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
- c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus..

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

Product Specification

8. Packing**8-1. Designation of Lot Mark**

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)

D : YEAR

F : PANEL CODE

H : ASSEMBLY CODE

E : MONTH

G : FACTORY CODE

I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one Pallet : 13 pcs

b) Pallet Assy Size : 1140 mm X 990 mm X 810 mm.

Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
 And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
 (if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

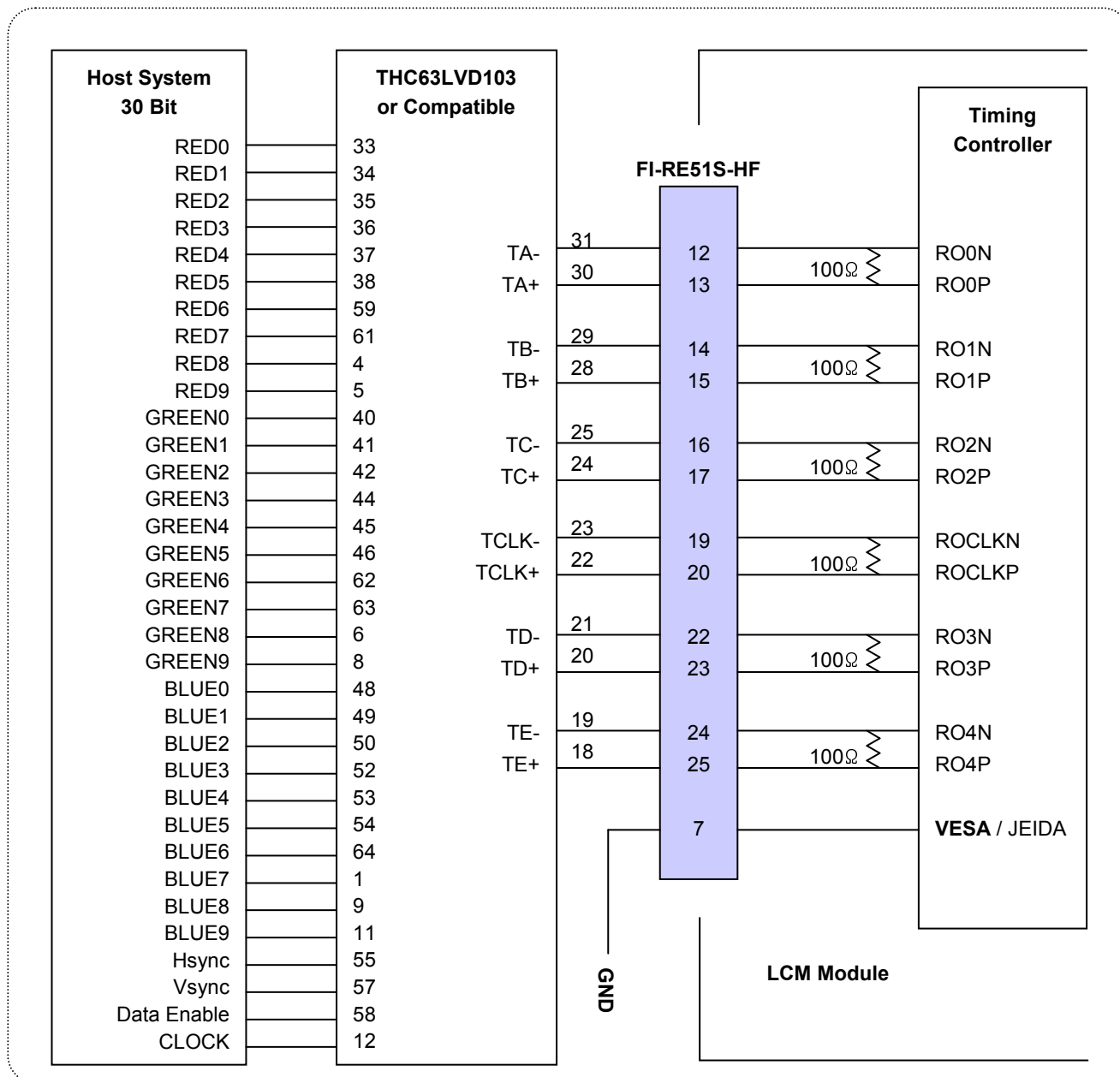
9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Product Specification

APPENDIX-I-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L")



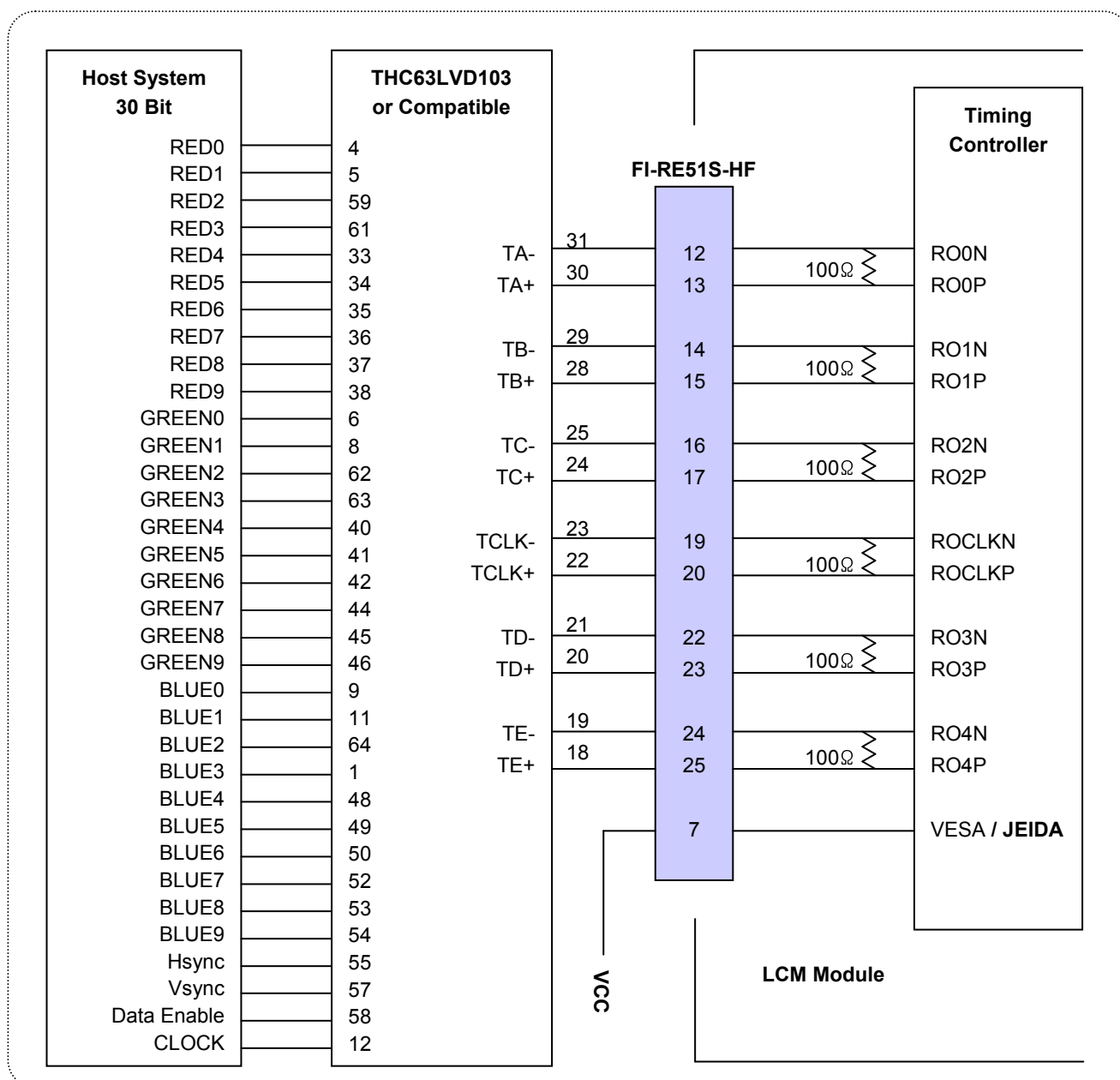
Notes:

1. The LCD module uses a 100 Ohm(Ω) resistor between positive and negative lines of each receiver input.
2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

APPENDIX-I-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")



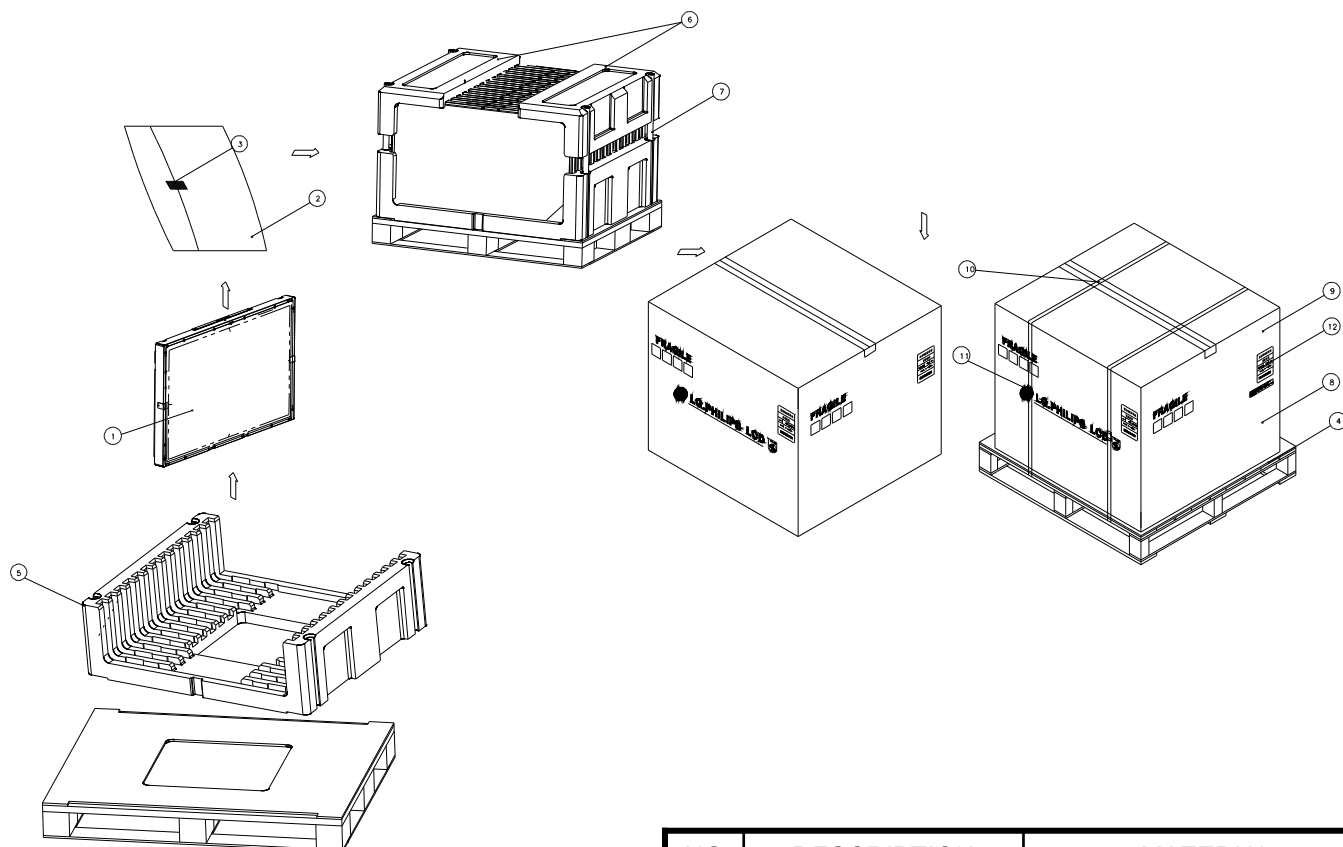
Notes:

1. The LCD module uses a 100 Ohm(Ω) resistor between positive and negative lines of each receiver input.
2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

APPENDIX-II

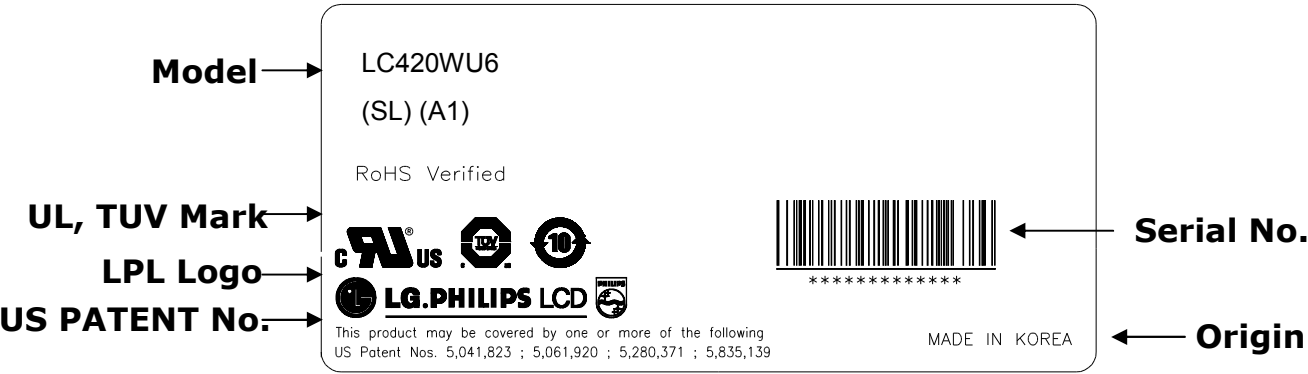
■ Pallet Ass'y



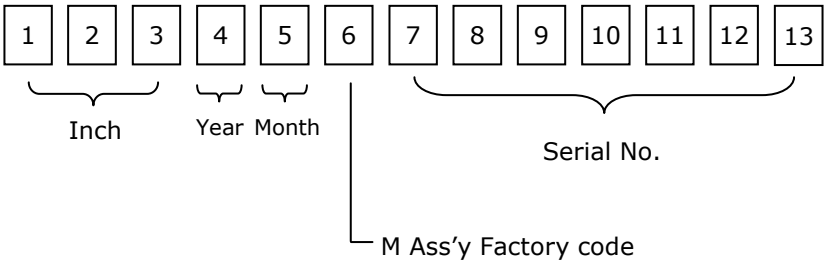
NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	42INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	PAPER 1140X990X130MM
5	PACKING,BOTTOM	EPS
6	PACKING, TOP	EPS
7	ANGLE, POST	PAPER
8	ANGLE, PACKING	PAPER
9	BAND, CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO 80G 100X100

APPENDIX- III

■ LCM Label

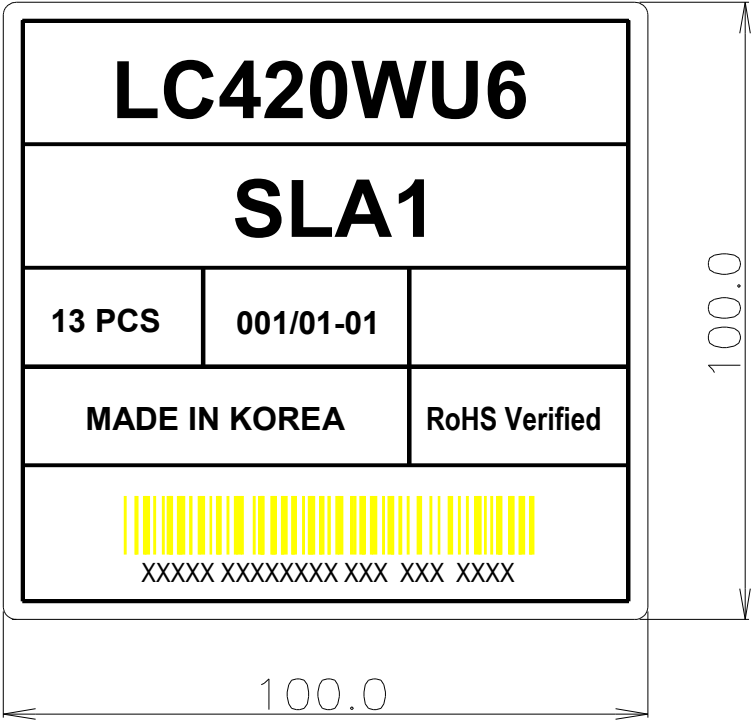


■ Serial No. (See CAS 25page for more information)



APPENDIX- IV

■ Pallet Label



Product Specification

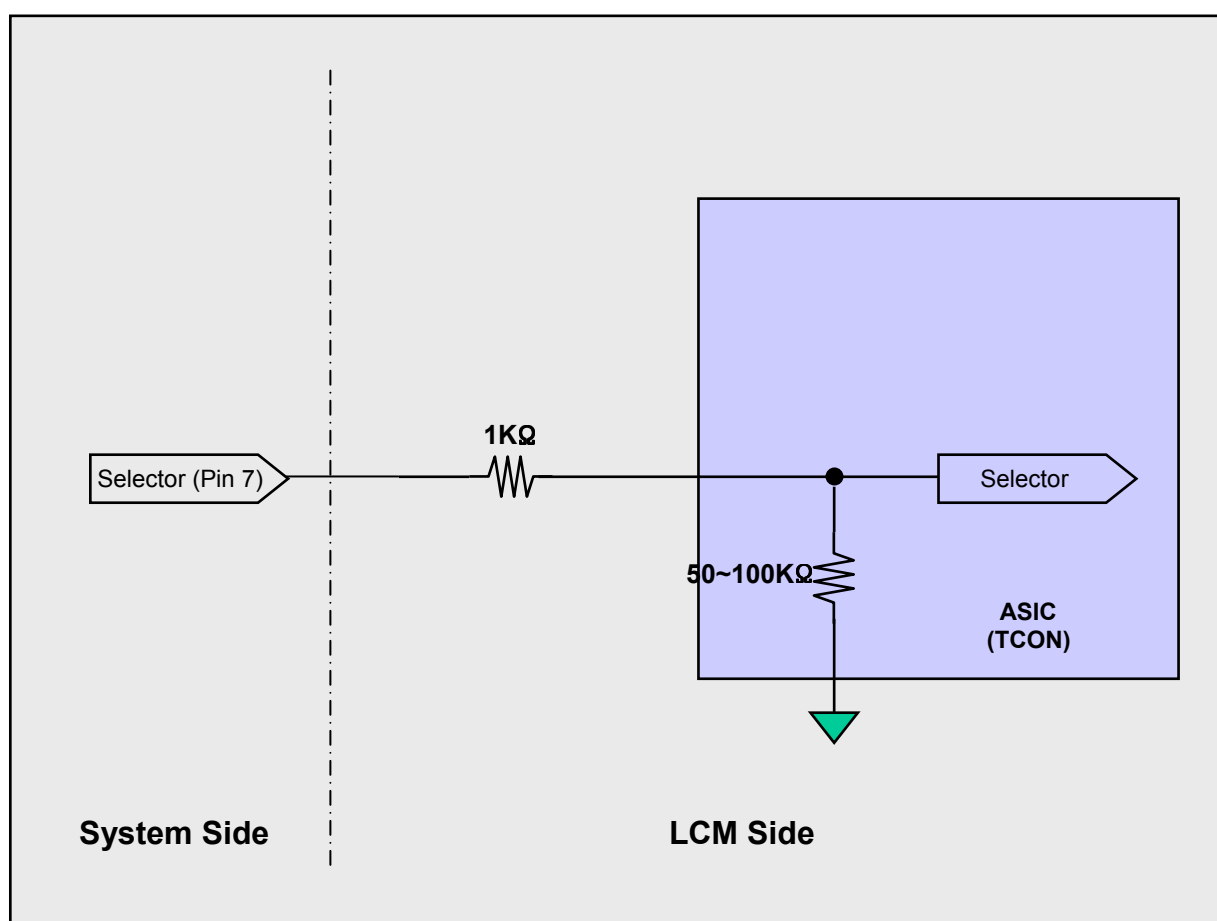
APPENDIX- V

No.	Item	Code	Unit	Variational Characteristics		Remark
				Frequency	Characteristics	
1	Lamp Voltage	VL	Vrms	Fr=65kHz	1620±10% (3.0mA) 1630±10% (4.0mA) 1640±10% (5.0mA) 1660±10% (6.0mA) 1690±10% (7.0mA) 1710±10% (7.3mA) 1740±10% (8.0mA)	Note 1
				Fr=45kHz	1680±10% (3.0mA) 1715±10% (4.0mA) 1770±10% (5.0mA) 1850±10% (6.0mA) 1945±10% (7.0mA) 1980±10% (7.3mA) (2060±10%) (8.0mA)	
2	Established Starting Voltage	Es	Vrms	Fr=65kHz	2080 Max. (25°C) 2500 Max. (0°C)	Note 8
				Fr=45kHz	2180 Max. (25°C) 2620 Max. (0°C)	
3	Lamp Current	IL	mArms	7.3 constant		
4	Lamp Power (Reference Value)	WL	W	Fr=65kHz	(4.3) (3.0mA) (5.6) (4.0mA) (6.9) (5.0mA) (8.1) (6.0mA) (9.2) (7.0mA) (9.6) (7.3mA) (10.3) (8.0mA)	Note 1
				Fr=45kHz	(4.1) (3.0mA) (5.4) (4.0mA) (6.5) (5.0mA) (7.6) (6.0mA) (8.7) (7.0mA) (9.0) (7.3mA) (9.6) (8.0mA)	
5	Luminance	B	cd/m ²	Fr=65kHz	7650±10% (3.0mA) 11800±10% (4.0mA) 15600±10% (5.0mA) 19100±10% (6.0mA) 22000±10% (7.0mA) 22900±10% (7.3mA) 24800±10% (8.0mA)	Note 1,2
				Fr=45kHz	7730±10% (3.0mA) 12100±10% (4.0mA) 15600±10% (5.0mA) 19100±10% (6.0mA) 22000±10% (7.0mA) 22900±10% (7.3mA) (24800±10%) (8.0mA)	

APPENDIX- VI

■ Option Pin Circuit Block Diagram

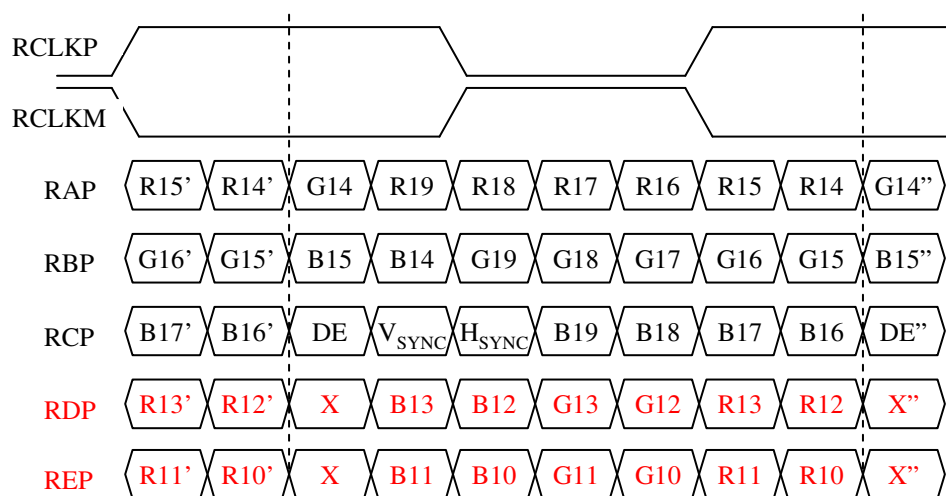
Circuit Block Diagram of LVDS Format Selection pin



APPENDIX- VII

LVDS Data-Mapping info. (10bit)

■ LVDS Select : “H” Data-Mapping (JEIDA format)



■ LVDS Select : “L” Data-Mapping (VESA format)

